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## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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<b>Title of Invention</b>	METHOD AND MANUFACTURE OF THIN SILICON ON INSULATOR (SOI) WITH RECESSED CHANNEL AND DEVICES MANUFACTURED THEREBY
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Application Number : 10/605,726

Confirmation Number:

First Named Applicant: Huilong Zhu

Attorney Docket Number: FIS920030303US1

Art Unit: 2813

Examiner:

Search string: ( 6465311 or 6391720 or 6225173 or 4616400 or 5814544 ).pn

### US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<i>SH</i>	1	6465311	2002-10-15	Shenoy	B2	—	—
<i>SH</i>	2	6391720	2002-05-21	Sneelal, et al.	B1	—	—
<i>SH</i>	3	6225173	2001-05-01	Yu	B1	—	—
<i>SH</i>	4	4616400	1986-10-14	Macksey, et al.	A	—	—
<i>SH</i>	5	5814544	1998-09-29	Huang	A	—	—

### Signature

Examiner Name	Date
<i>Stephen W. Smoot</i>	February 4, 2005